

April 17, 1956

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2,742,616

NEGATIVE IMPEDANCE REPEATERS

Original Filed Aug. 30, 1949

5 Sheets-Sheet 1

FIG. 1

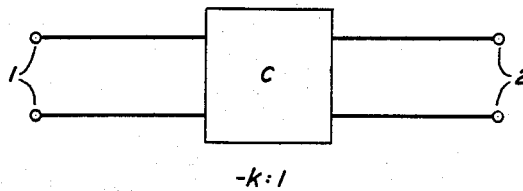


FIG. 2

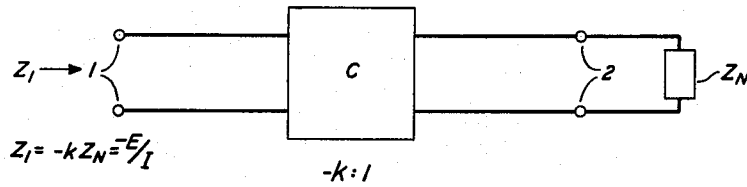


FIG. 3

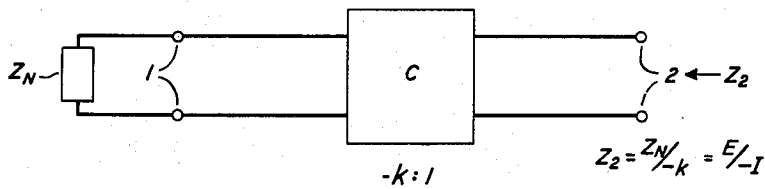
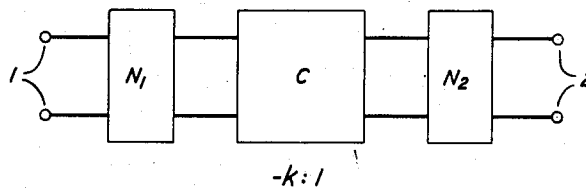


FIG. 4



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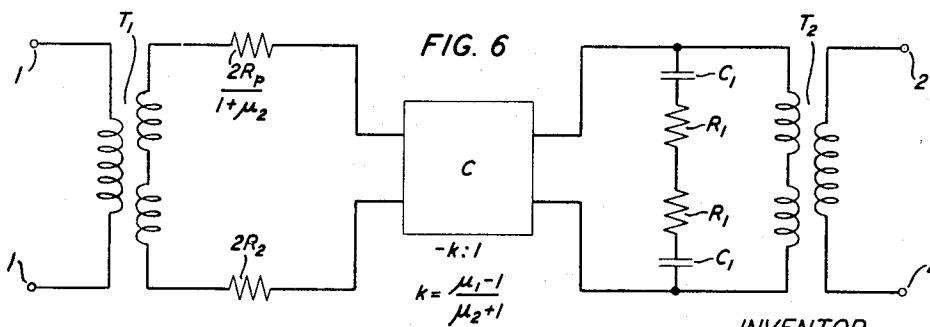
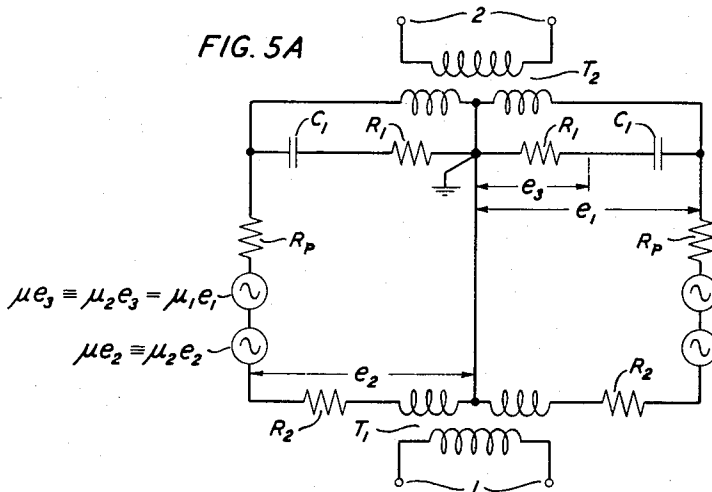
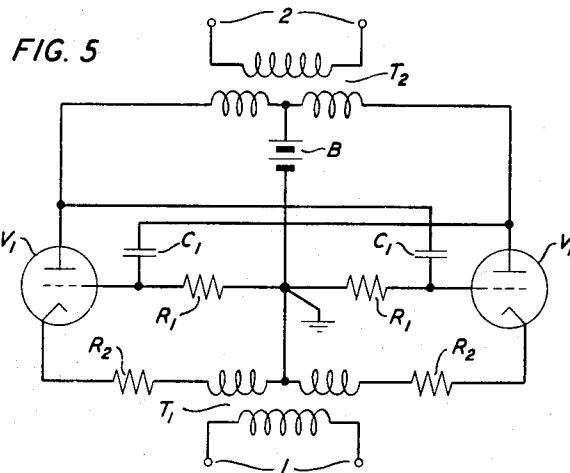
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NEGATIVE IMPEDANCE REPEATERS

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5 Sheets-Sheet 3

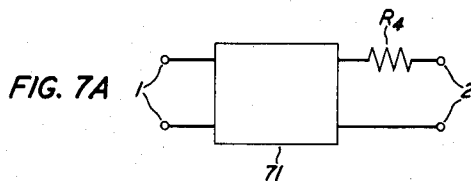
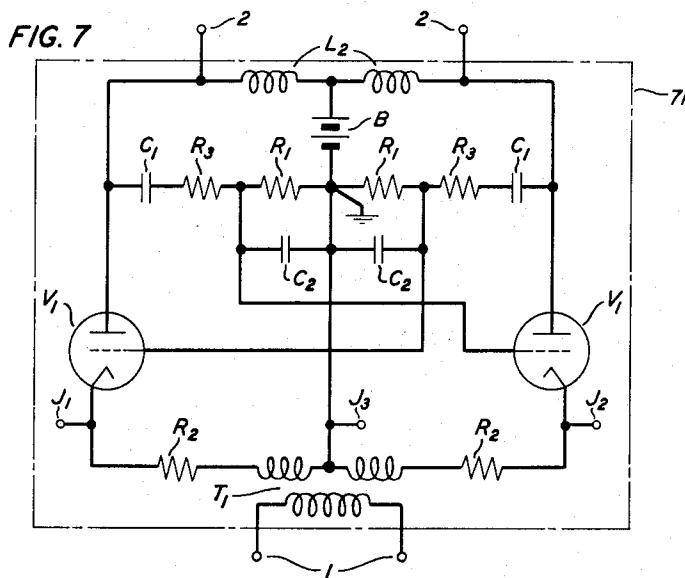
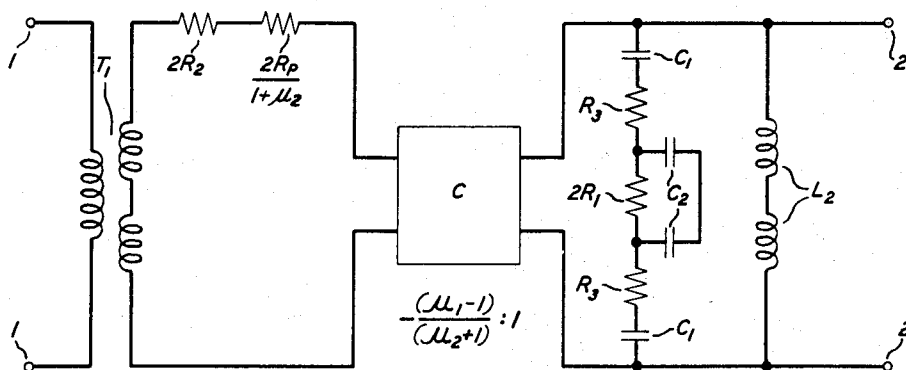


FIG. 8



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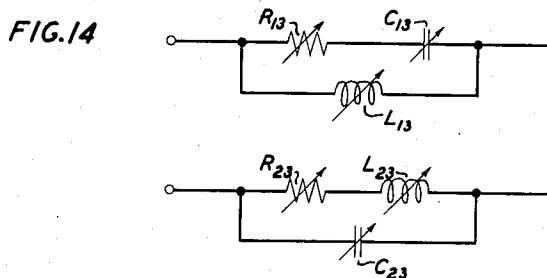
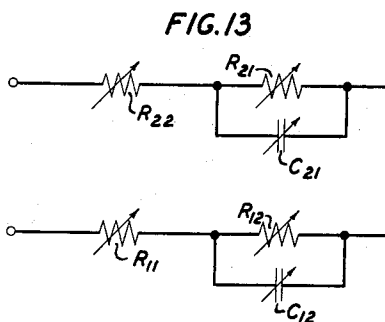
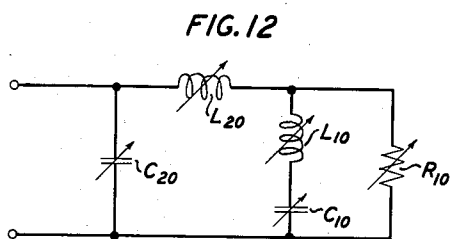
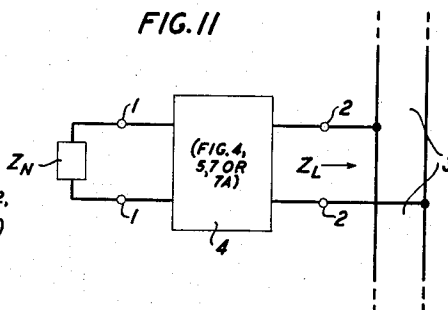
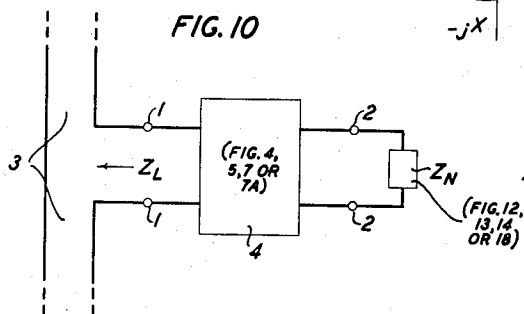
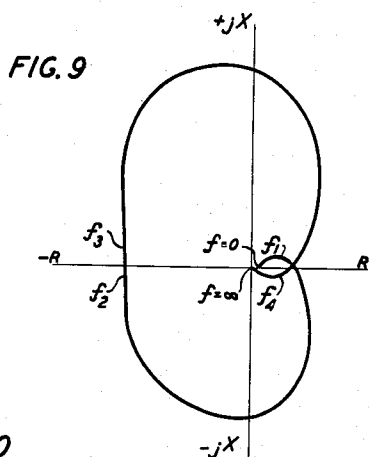
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NEGATIVE IMPEDANCE REPEATERS

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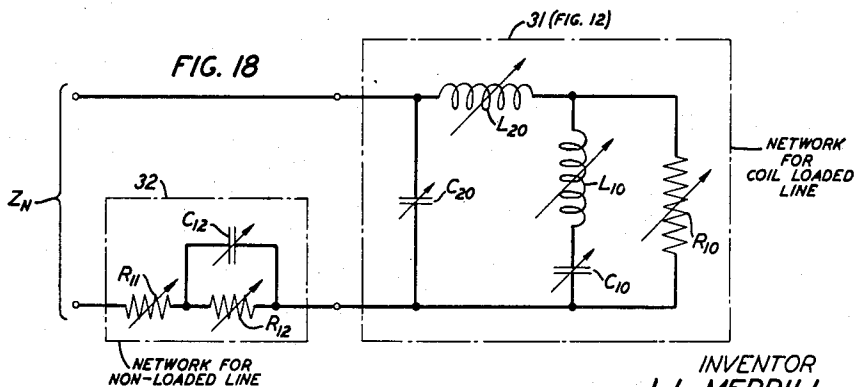
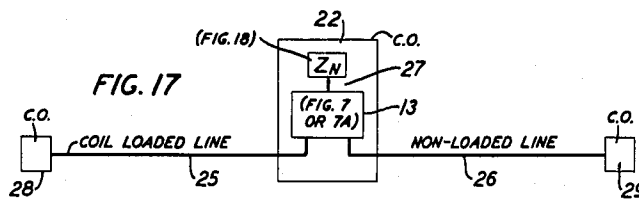
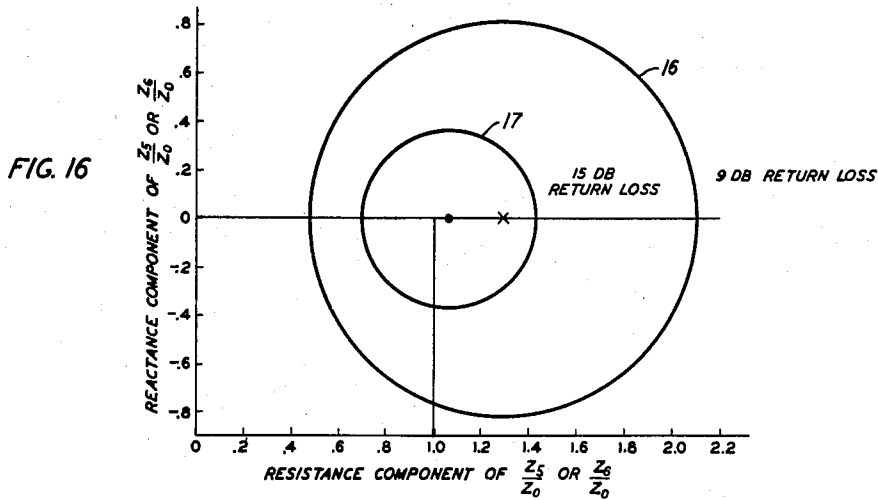
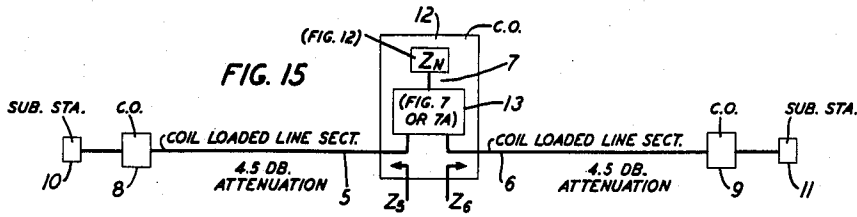
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NEGATIVE IMPEDANCE REPEATERS

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5 Sheets-Sheet 5



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1

2,742,616

NEGATIVE IMPEDANCE REPEATERS

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Original application August 30, 1949, Serial No. 113,072, now Patent No. 2,582,498, dated January 15, 1952. Divided and this application October 23, 1950, Serial No. 191,670

10 Claims. (Cl. 333—80)

This invention relates to negative impedance circuits such as, for example, negative impedance converters, negative impedance repeaters and circuits incorporating them, and transmission lines loaded with negative impedances.

Objects of the invention are production of stable negative impedance, and reduction of attenuation or distortion in transmission lines.

It is also an object of the invention to provide a stable transmission system comprising a vacuum tube type of negative impedance repeater connected in series between two inductively loaded lines or in series between an inductively loaded line and a non-loaded line.

The present application is a division of application Serial No. 113,072, filed August 30, 1949 (issued January 15, 1952, as United States Patent 2,582,498).

In one aspect the invention is a specific form of vacuum tube negative impedance converter, i. e., vacuum tube circuit for converting positive impedance into negative impedance. The practical (i. e., real or actual) converter is reducible to an electrically equivalent four-terminal circuit consisting of positive impedance elements together with a device that may be referred to as an ideal negative impedance converter. The ideal negative impedance converter is a four-terminal network or device that has an impedance transformation ratio of $-k:1$, k being a quantity that is a numeric at a prescribed frequency and approximately a numeric over a finite frequency range which includes the prescribed frequency, but that at frequencies below and above this range is a complex quantity which can have an appreciable phase angle. In the first-mentioned equivalent circuit, some of the positive impedance elements appear as a network on one side of the ideal converter; the remainder appear as a network on the other side of the ideal converter. These two networks tend to make the ratio of impedance transformation for the practical converter's equivalent circuit depart from the ratio for the ideal converter.

In accordance with a feature of the invention, the practical converter can be so constructed that, except with regard to power dissipation, the two networks in its equivalent circuit substantially balance each other in effect over the frequency range of interest. Thus only the effect of the ideal converter remains and the practical converter can be represented by the ideal converter.

In accordance with a feature of the invention, to facilitate obtaining this balance or to reduce the effect of unbalance of the two networks, the impedance of series arms of the networks may be made relatively low and the impedance of shunt arms relatively high.

In accordance with a feature of the invention, the converter is constructed to develop a negative impedance over a prescribed frequency range and positive impedance outside this frequency range. Such impedance control is desirable for a number of reasons. For example, at extremely high or low frequencies the positive impedance elements of the converter determine the impedance seen at its terminals. For many practical purposes, it is not only necessary to have the impedance seen at the termi-

2

nals negative over a prescribed range of frequencies, which is the range of primary interest, but it is also necessary to have the impedance seen at the terminals positive at frequencies outside this band. A positive impedance at high and low frequencies may be desired for two reasons: first, to insure stability against oscillation; and second, to attenuate or pass other signals at frequencies where gain may not be wanted. For example, in a telephone line gain may be required for the voice band of frequencies, but not wanted at the lower frequencies of ringing, dialing, and the like. The reason for this is that the power handling capacity required of a negative impedance converter to increase ringing and dialing currents would have to be much greater than that required to provide gain for speech currents only. Ringing generators or dial pulse repeaters might prove more economical for supplying power at these lower frequencies.

One specific form of negative impedance converter embraced by the invention is an electric space discharge amplifier comprising a negative feedback impedance common to the cathode-anode and cathode-grid circuits and adapted to serve as an input coupling circuit and output coupling circuit for the amplifier, an anode circuit load impedance in serial relation with the feedback impedance in the cathode-anode circuit, and means for producing in the amplifier positive feedback that renders the amplifier input impedance negative over a prescribed frequency range, the means for producing positive feedback comprising a positive feedback path whose input voltage depends upon and is derived from the anode circuit load impedance. The negative feedback reduces the amplifier input impedance to a low value and the positive feedback further reduces it, rendering it negative over the prescribed frequency range. The anode circuit load impedance includes an impedance network for controlling the magnitude and phase of the amplifier input impedance, in order, for example, to give gain control and attenuation equalization when the converter is connected as a repeater in series in a telephone transmission line. The repeater, while of general utility, is particularly suitable for use in exchange area circuits of telephone systems, where gain with stability against oscillation is difficult to obtain because the impedances encountered by repeated lines vary widely as a result of the great variety of facilities to be interconnected or switched.

Other objects, aspects and features of the invention will be apparent from the following description and claims.

Fig. 1 shows a basic or ideal negative impedance converter;

Fig. 2 shows the impedance seen at terminals 1 of the ideal converter;

Fig. 3 shows the impedance seen at terminals 2 of the ideal converter;

Fig. 4 shows the equivalent circuit of a practical converter;

Figs. 5 and 6 show the circuit schematic and equivalent circuit respectively, of a practical negative impedance converter, and Fig. 5A shows the circuit of Fig. 5 with its triodes represented by their equivalent circuits;

Figs. 7 and 8 show the circuit schematic and equivalent circuit of another practical negative impedance converter, and Fig. 7A shows a modification of the circuit of Fig. 7;

Fig. 9 shows the impedance characteristic plotted in polar form as seen at terminals 1 of Fig. 7 with a resistance connected to terminals 2;

Figs. 10 and 11 show converters respectively in series and in shunt with a line;

Figs. 12, 13 and 14 show networks for use with a negative impedance converter connected in series with a transmission line;

Fig. 15 shows a negative impedance repeater in series between inductively loaded line sections;

Fig. 16 shows return loss circles for facilitating explanation of Figs. 15;

Fig. 17 shows a negative impedance repeater in series between an inductively loaded line and a non-loaded line; and

Fig. 18 shows an impedance network for use in the repeater in Fig. 17.

Fig. 1 presents an ideal negative impedance converter C. It is a form of transformer but has a negative ratio of impedance transformation or conversion designated $-k:1$, as shown. Like a transformer, the converter C can have four terminals. It is capable of bilateral transmission. As shown in Fig. 2, if a positive impedance Z_N is connected to terminals 2, $-kZ_N$ is seen at terminals 1. As shown in Fig. 3, if a positive impedance Z_N is connected to terminals 1, $Z_N/-k$ is seen at terminals 2. As noted by G. Crisson (Negative Impedance and the Twin 21-Type Repeater—B. S. T. J.—July 1931) there are two types of negative impedance, the series type and the shunt type. It is desired to point out the fact that if impedance is defined as $Z=E/I$ then negative impedance can be either Z multiplied by -1 , (i. e., $-Z = -E/I$) or Z divided by -1 , (i. e., $-Z=E/-I$). The negative impedance seen at terminals 1, Fig. 2, is the series or reversed voltage type, $-E/I$. The negative impedance seen at terminals 2, Fig. 3, is the shunt or reversed current type, $E/-I$.

Any practical (i. e., real or actual) vacuum tube converter contains positive impedance elements in its equivalent circuit along with the ideal converter. This is illustrated by Fig. 4, which shows the equivalent circuit of a practical converter. Some of these positive impedance elements appear as a network N_1 on the left-hand side of the ideal converter; the others appear as a network N_2 on the right-hand side. Ordinarily these networks N_1 and N_2 are such that they tend to make the ratio of transformation for the practical converter's equivalent circuit depart from the ratio for the ideal converter. If networks N_1 and N_2 , when viewed from the converter C, had the same configuration and had the impedance of each element in N_1 equal k times the impedance of the correspondingly located element in N_2 , then (except with regard to power dissipation) these networks N_1 and N_2 would balance each other in effect, thus cancelling out, so only the effect of the ideal converter would remain and Fig. 4 could be represented by Fig. 1; or in other words, then the practical converter would be such that in its equivalent circuit each of the networks N_1 and N_2 as seen from the other (through the ideal converter) would neutralize the effect of the other (upon the transformation ratio of the equivalent circuit of the practical converter), and so the impedance transformation ratio for the practical converter would be the same as for the ideal converter. As can be seen from Figs. 5 and 6, described below, difficulty might be encountered were it attempted to make the practical converter such that N_1 and N_2 in its equivalent circuit would, when viewed from C, have like configurations and have the impedance of each element in N_1 equal k times the impedance of the correspondingly located element in N_2 , (for example, were it attempted to add to each network the elements required for giving it the same configuration as the other network when the two networks are viewed from C, and then assign each element in N_1 an impedance value equal to k times that of the correspondingly located element in N_2). However, in accordance with a hereinafter described feature of the invention, the practical converter can be readily made such that, in its equivalent circuit, over the frequency range of interest N_1 and N_2 mutually substantially cancel or neutralize their effects on the transformation ratio. This can be accomplished by constructing the practical converter so that in the networks N_1 and N_2 of its equivalent circuit the following conditions obtain over the frequency range of interest: (1) certain of the series and shunt elements have their impedances

low and high, respectively, compared to each of the two impedances between which the converter is to be connected, so the effects of those series and shunt elements on the impedance transformation ratio of the equivalent circuit of the practical converter are negligibly low; and (2) the remaining elements (of the networks N_1 and N_2) have their impedances and their positions in the network configurations such that the remaining elements in each network substantially cancel or neutralize the effect of the remaining elements in the other network upon the over-all transformation ratio of the equivalent circuit of the practical converter.

Fig. 5 shows the circuit schematic of a practical negative impedance converter embodying one specific aspect of the invention. It comprises: two transformers designated T_1 and T_2 ; two identical vacuum tubes or electric space discharge devices preferably normally biased for class A operation, each designated V_1 ; two similar capacitors designated C_1 , each capacitor coupling the plate of one tube to the grid of the other, for producing positive feedback; two similar resistors R_1 , respectively connecting the grids of the tubes to the negative terminal of the battery B; and two resistors R_2 , one in each cathode circuit, for grid bias. The negative pole of the battery B is shown grounded. The devices V_1 are in push-pull relation. One winding of transformer T_1 is connected in series with the resistors R_2 between the cathodes and has its mid-point grounded. The resistors R_2 and the two halves of this winding produce negative feedback and produce direct-current voltage drops for biasing the grids of the two tubes equally. The direct-current resistance of one of the halves of the winding may exceed that of the other half by a given amount, and then the resistance of the element R_2 adjacent that other half may exceed by the same amount the resistance of the other element R_2 . The devices V_1 may be, for example, twin triodes of a Western Electric type 407A vacuum tube, which has the amplification constant μ of each triode equal to approximately 30.

While the discharge devices shown in Fig. 5 (and those shown in Fig. 7 which is described hereinafter) have but one grid, the term triode in the specification and claims is generic to multigrid discharge devices, for example, tetrodes and pentodes which include a cathode, an anode and a grid or space discharge control element.

Let μ_1 represent the amplification from the plate-to-ground voltage of either vacuum tube to the resulting component of the internal plate-cathode generator voltage in the other tube, and let μ_2 represent the amplification from the cathode-to-ground voltage of either tube to the resulting component of the internal plate-cathode generator voltage in the tube. Thus, μ_2 is the factor by which the voltage between cathode of either tube and ground (or the negative pole of the battery B) must be multiplied in order to obtain the value of the resulting component of internal plate generator voltage in that tube, (or in other words, μ_2 is the tube amplification constant, usually designated μ); and μ_1 is the quantity by which the voltage between the plate of either tube and ground (or the negative pole of the battery B) must be multiplied in order to obtain the component of internal plate generator voltage of the other tube that results from the voltage drop between its grid and ground. The amplification factor μ_1 is equal to $\beta\mu_2$ where β designates the ratio of the voltage between ground and the grid of either tube to the voltage between ground and the plate of the other tube. Representing the triodes by their equivalent circuits in conventional manner, the circuit of Fig. 5 can be reduced to that of Fig. 5A. In Fig. 5A, the voltage between ground and the cathode of one tube is designated e_2 , the voltage between ground and the plate of the other tube is designated e_1 , and the voltage across the resistor R_1 in the grid circuit of that other tube is designated e_3 . The plate generator in the one tube is indicated as two generators in series, one be-

5

ing designated by its voltage μe_2 and the other being designated by its voltage μe_3 , the total plate generator voltage in this tube being $\mu e_2 + \mu e_3$. It is seen that

$$\mu_2 e_3 = (\mu_2) \left(\frac{e_3}{e_1} \right) e_1 = \mu_2 \beta e_1 = \mu_1 e_1$$

In the specific circuit of Fig. 5,

$$\beta = \frac{e_3}{e_1} = \frac{R_1}{R_1 + \frac{1}{\omega C_1}}$$

where ω designates the angular velocity in radians. Thus, μ_1 , the amplification of the voltage in the plate circuit, depends upon the relative values of C_1 and R_1 as well as on μ , the tube amplification constant.

Fig. 6 shows the equivalent circuit of Fig. 5 as derived by application of circuit theory to Fig. 5A. Fig. 6 comprises the transformers, capacitors and resistors mentioned above plus an ideal negative impedance converter C having a ratio of transformation of

$$-(\mu_1 - 1)/(\mu_2 + 1):1$$

In addition, the internal plate resistance of the tubes appears as an impedance in series with transformer T_1 , this impedance being shown as a resistance of value

$$2Rp/(1 + \mu_2)$$

the value of the plate resistance in each tube being taken as R_p . If μ_1 approximates μ_2 and each is much greater than unity, k approximates unity. If $k=1$ and

$$2Rp/(1 + \mu_2)$$

is of small order of magnitude relative to the impedance that it faces (i. e., relative to the sum of all impedances effectively in series with it), then the action of the converter, to a first approximation at least, is independent of minor variations in tube constants and battery supply voltage. Making μ_2 large and R_p small tends to reduce the impedance of the element identified in the drawing as $2Rp/(1 + \mu_2)$ and thus render its effect on the negative impedance presented by the converter unimportant. In the circuit of Fig. 6, all elements on the left-hand side of the ideal converter C may be designated as a network N_1 and all elements on the right-hand side of the ideal converter C may be designated as a network N_2 , after the fashion of Fig. 4. As indicated above, it is apparent that difficulty would be encountered were attempt made to so construct the circuit of Fig. 5 that in its equivalent circuit shown in Fig. 6, N_1 and N_2 when viewed from C would have the same configuration and have the impedance of each element in N_1 equal k times the impedance of the correspondingly located element in N_2 .

However, if all series elements in the circuit of Fig. 6 can be made relatively small in impedance and all shunt elements relatively large the circuit will approach that of the ideal converter. In other words, the operation or effect of the circuit approaches that of the ideal converter provided that, when the transformer T_1 and T_2 are replaced by their usual equivalent networks, all impedances (of elements of networks N_1 and N_2) effectively in series in the circuit with respect to transmission between terminals 1 and 2 are much smaller than each of the two impedances to be attached to terminals 1 and 2, and all impedances effectively in shunt across the circuit with respect to transmission between terminals 1 and 2 are much greater than each of the just-mentioned two impedances. At high frequencies a practical difficulty arises. The windings of transformers T_1 and T_2 have distributed capacity and leakage inductance. At some frequency this capacity and inductance will resonate. If this frequency is not identically the same for T_1 and T_2 , the circuit may be unstable and oscillate as explained below.

A converter of the type described herein is essentially a feedback amplifier and as such must meet Nyquist's

6

rule for stability (given in the article by H. Nyquist on Regeneration Theory, B. S. T. J., January 1932). However, with reference to the ideal converter there is a similar rule which can be applied in order to determine

unconditional stability. Referring to the ideal converter of Fig. 2, assume a line or other circuit of impedance Z_L (not shown in Fig. 2) is connected to terminals 1. Then if kZ_N were equal to Z_L it is evident that the impedance converter provided that, when the transformers T_1 and T_2 are replaced by their usual equivalent networks, all impedances (of elements of networks N_1 and N_2) effectively in series in the circuit with respect to transmission between terminals 1 and 2 are much smaller than each of the two impedances to be attached to terminals 1 and 2, and all impedances effectively in shunt across the circuit with respect to transmission between terminals 1 and 2 are much greater than each of the just-mentioned two impedances. At high frequencies a practical difficulty arises. The windings of transformers T_1 and T_2 have distributed capacity and leakage inductance. At some frequency this capacity and inductance will resonate. If this frequency is not identically the same for T_1 and T_2 , the circuit may be unstable and oscillate as explained below.

From a practical engineering viewpoint there is a criterion for judging stability which is often more useful than the general rule. It can be stated as follows: The ideal negative impedance converter will be unconditionally stable providing that the magnitude of kZ_N/Z_L is less than unity at any frequency where the angle of this ratio is zero.

In a practical converter, as shown in Figs. 4, 5 or 6, the same rule for stability holds except Z_L is to be taken as the impedance seen looking toward network N_1 from the ideal converter C and Z_N is to be taken as the impedance seen looking toward network N_2 from the ideal converter C. If the elements in N_2 are all made equal to or less than $1/k$ times corresponding elements in N_1 in impedance at all frequencies, then these two networks can be omitted from stability considerations for many practical purposes. Otherwise, the effect of N_1 must be included in Z_L and the effect of N_2 must be included in Z_N , in applying the stability rule. If at any frequency a resonant condition then exists whereby the impedance Z_N goes to a high value there will be the possibility of kZ_N being greater than Z_L . If when this condition occurs the angle of the ratio kZ_N/Z_L is zero the circuit may oscillate. Therefore, it is desirable to prevent such resonance from occurring in the network N_2 of Fig. 4.

One way of accomplishing this is illustrated in Figs. 7 and 8. The Fig. 7 is similar to Fig. 5 except that a retard coil or inductance coil L_2 has been substituted for transformer T_2 , resistor R_3 has been inserted in series with capacitor C_1 , and capacitor C_2 has been shunted across R_1 . The network C_1 , R_3 and R_1 largely determines the value of μ_1 at low frequencies. The network R_3 , R_1 and C_2 largely determines the value of μ_1 at high frequencies. (As in the case of Fig. 5, $\mu_2 = \mu$, and μ_1 is the amplification from the plate-to-ground voltage of either triode to the resulting component of the internal plate-cathode generator voltage in the other triode. The equivalent circuit of Fig. 7 has been derived by application of circuit theory and is shown in Fig. 8. On the right-hand side of the ideal converter C all reactance elements are in shunt paths across the terminals 2. An antiresonance can occur but in any such case the impedance on the right-hand side of the ideal impedance converter will be determined primarily by the network attached to terminals 2. It is an important feature of Fig. 7 that in its equivalent circuit shown as Fig. 8, in the circuit between the ideal converter C and the terminals 2 there is no impedance effectively in series that might, by resonating with capacitance effectively in shunt across the circuit, cause the impedance on the right-hand side of C to be greater than that on the left-hand side and

thereby create possibility of instability or singing. In Fig. 6, in contrast, leakage inductance of transformer T₂, effectively in series in the circuit between the ideal converter C and the terminals 2, might resonate at a high frequency with shunt capacitance (distributed capacitance of windings of T₂) and thus form an antiresonant circuit (including Z_N) across the right-hand terminals of converter C, and thereby cause the impedance on the right-hand side of converter C to exceed 1/k times that on the left-hand side (and so create a potential singing condition that would require careful consideration in the design of the converter and its associated circuits).

In Fig. 8, as in Fig. 6, the ratio of transformation $-k$ equals $-(\mu_1 - 1)/(\mu_2 + 1)$, where μ_1 depends upon the values of the impedances in the RC circuit coupling the grids and plates of the vacuum tubes (as well as upon the amplification factor of the tubes themselves). At high and low frequencies, μ_1 is not a numeric but is a complex quantity whose angle and magnitude are determined at these frequencies largely by the values of the just-mentioned impedances. In the operating frequency range, if these impedance values are adjusted so μ_1 approximates μ_2 and each is much greater than unity, k approximates unity. If, over a definite frequency band, all shunt elements in Fig. 8 are made relatively large and all series elements relatively small in value as referred to above in connection with Fig. 6, and, furthermore, $k=1$, then the circuit of Fig. 8 (and correspondingly the circuit of Fig. 7) approximates in operation on this frequency band an ideal converter having a ratio of transformation of -1 times the ratio of the impedance of the line winding of transformer T₁ facing terminals 1 to the impedance of the other winding of transformer T₁. If k is close to unity and the impedance $R_p/(1+\mu_2)$ is of small order of magnitude relative to the impedance that it faces, then battery supply variations and tube changes will have little effect upon the negative impedance presented by the converter. (As indicated above in connection with Fig. 6, making μ_2 large and R_p small, for example by appropriate choice of tube type and operating voltages, tends to render $2R_p/(1+\mu_2)$ negligibly small.)

Negative resistance can be obtained only over a finite range of frequencies. For example, when a resistance (not shown) is connected to terminals 2 of Fig. 8, the impedance seen looking into terminals 1 resembles the locus shown on the polar diagram of Fig. 9. Between a frequency f_2 and a higher frequency f_3 , there is seen at terminals 1 an impedance which approximates a negative resistance, and at some frequencies between f_2 and f_3 a pure negative resistance is found. At zero frequency the impedance seen is a small positive resistance equal to the direct-current resistance of the primary winding of transformer T₁. At a low frequency f_1 the locus shows a positive impedance. The admittance corresponding to this portion of the impedance locus can be used, for example when terminals 1 are in series in a telephone transmission line, for the passage of low frequency currents for ringing, dialing, and the like. At high frequencies f_4 the impedance locus approaches the origin, the impedance approaching zero through capacitive reactance. At high frequencies above the band passed by the telephone line, ordinarily it is desirable that the impedance be positive because gain at those high frequencies is not useful and may be detrimental in adding to the difficulty of obtaining stable operation. As explained hereinafter, when terminals 1 are used in series with a voice frequency transmission line, the impedance most suitable for use across terminals 2 ordinarily will not be a pure resistance but will be a network presenting a complex impedance.

Between frequencies such as f_2 and f_3 the network across terminals 2 most accurately controls the negative impedance. Therefore, the main transmission band where negative impedance is desired, ordinarily will have its center between two such frequencies and preferably lie be-

tween them. For example, in the case of a converter employed in a voice frequency negative impedance repeater, the frequencies on its impedance locus that corresponds to f_2 and f_3 may be 300 cycles per second and 4,000 cycles per second, respectively, and the phase angle of the negative impedance at each of these two frequencies may differ from 180 degrees by some five or ten degrees. Of course, gain can be had over a band wider than from 300 to 4,000 cycles per second if desired.

In one specific practical design of converter circuit of the type of Fig. 7, over the voice band of frequencies μ_1 is approximately equal to μ_2 , and because μ_1 and μ_2 are large compared to unity the ratio of transformation of the converter

$$-\frac{\mu_1 - 1}{\mu_2 + 1} \cdot 1$$

equals about $-0.9:1$ at voice frequencies. The devices V₁ are twin triodes of a Western Electric type 407A vacuum tube for which μ is approximately 30. The impedance ratio of the line transformer T₁ is 1:9 step up from the line winding to the winding conductively connected to the cathodes. In the equivalent circuit of the converter, the shunt arms of the equivalent networks on each side of the ideal converter are high impedances at voice frequencies and can be neglected. To cancel the effect of the series resistances on the left-hand side of the ideal converter in this circuit, a series resistance 1/k times as great (in this case about 2,000 ohms) is needed on the right-hand side of the ideal converter. In the practical circuit this resistance is added as shown at R₄ in Fig. 7A, wherein block 71 is the same circuit as block 71 in Fig. 7. Thus, in Fig. 7A, if a network Z_N of impedance value Z_N be connected across terminals 2, for example as indicated in Fig. 2 or in Figs. 10, 11, 15 or 17 described hereinafter, then at voice frequencies the impedance of terminals 1 viewed from the line equals approximately $-0.1Z_N$. By inserting a resistance R₄ such as that of Fig. 7 in the circuit of Fig. 8, as a series element of the circuit, for example between the upper terminal 2 and the junction of C₁ and L₂; the circuit of Fig. 8 is so modified as to become equivalent to the circuit of Fig. 7A, and in such equivalent circuit the resistance R₄ will be a part of the network N₂, the network between the ideal converter C and the terminals 2. In such equivalent circuit, the network N₂ is adapted to neutralize over a prescribed frequency range (the speech frequency range or the frequency range of interest) the effect of the network N₁ on the over-all transformation ratio of the equivalent circuit of the converter. In such equivalent circuit the networks N₁ and N₂ have series and shunt impedance elements (transformer T₁ may be replaced by its usual equivalent T network), certain of the series and shunt elements having their impedances low and high, respectively, compared to each of the two impedances between which the converter is to be connected (i. e., the impedance to be connected across terminals 1 and that to be connected across terminals 2), and the remaining impedance elements in the two networks N₁ and N₂ having their impedances such that the remaining elements in each network are adapted substantially to neutralize the effect of the remaining elements in the other network upon the over-all transformation ratio of the equivalent circuit of the practical converter of Fig. 7A (for example, the remaining element R₄ in network N₂ is equal to 1/k times the sum of the resistance 2R₂, the resistance

$$\frac{2R_p}{1+\mu_2}$$

and the resistances in the series arms of the equivalent T network of the transformer T₁). In this equivalent circuit for the converter of Fig. 7A, over the speech frequency range each shunt arm of the networks N₁ and N₂ (including the shunt arm or item of the equivalent T net-

work of the transformer T_1 , the shunt arm comprising condensers C_1 , condensers C_2 , resistance $2R_1$ and resistances R_3 and the shunt arm L_2) is of high impedance compared to each of the two impedances between which the converter is to be connected, and the impedance of the series arms of the network N_1 equals k times the impedance of the series arms of the network N_2 , or in other words, R_4 equals $1/k$ times the sum of $2R_2$, the resistance

$$\frac{2R_2 p}{1 + \mu_2}$$

and the resistance of the series arms of the equivalent T network of the transformer T_1 ,

$$(k \text{ being } \frac{\mu_1 - 1}{\mu_2 + 1})$$

In the case of this equivalent network of the converter of Fig. 7A, as in the case of the equivalent network (Fig. 8) of the converter of Fig. 7, all reactance elements of the network N_2 (including the condensers C_1 and C_2 and the inductance L_2) are in shunt arms of that network, so all series arms of that network have negligible reactance or in other words that network has no series arms whose reactance is not negligible. In the case of the converters of Figs. 7 and 5, the converter preferably is such that in its equivalent circuit (Fig. 6 with its transformers T_1 and T_2 considered to be replaced by their usual equivalent T networks, and Fig. 8 with its transformer T_1 considered to be replaced by the usual equivalent T network) the impedance of all series elements of the circuit (between terminals 1 and ideal converter C and between C and terminals 2) is much smaller than the impedance to be attached to terminals 1 and than the impedance to be attached to terminals 2, and the impedances of all shunt arms of the circuit (between C and terminals 1 and between C and terminals 2) are much greater than the impedance to be attached to terminals 1 and the impedance to be attached to terminals 2.

Converters embraced in the invention include not only converters of push-pull form but also converters of single-sided form, as for example, the converter (not shown) obtainable by omitting from Fig. 5, the following elements on the right-hand side of the figure: V_1 , C_1 , R_1 , R_2 and the winding between R_2 and ground. However, in the case of Fig. 5 the push-pull form has important advantages, including: double the power output of the single-sided circuit (assuming tubes of like type for the push-pull and the single-sided circuits); power supply noise reduction due to push-pull operation; and especially the advantages that μ_1 is not dependent entirely upon the coupling factor between the two halves of the center-tapped winding of transformer T_2 , and that the effect of the three-winding transformer T_2 upon the transformation ratio of the converter can much more readily be balanced out or neutralized by a push-pull (three-winding) transformer T_1 than be the two-winding transformer which would result from omission of the winding between R_2 and ground. In the push-pull form of the converter as shown in Figs. 5 and 6, over a prescribed frequency range k may, for example, be made close to unity as indicated above, the sum of the impedances $2R_2$ and

$$\frac{2R_2 p}{1 + \mu_2}$$

may be made negligibly small and the admittance of the shunt path comprising condensers C_1 and resistances R_1 may be made negligibly small, as indicated above, and then the transformers T_1 and T_2 as seen from the ideal converter C may be made as nearly alike as practicable, so that each will substantially neutralize the effect of the other upon the transformation ratio of the converter.

Converters embraced in the invention include also the single-sided converter (not shown) obtainable by omitting

from Fig. 7 the following elements: the right-hand tube V_1 , the resistance R_2 at its cathode, the winding turns of transformer T_1 connected between that resistance and ground, and the elements C_1 , R_3 , R_1 and C_2 that couple the plate of the left-hand tube to the grid of the right-hand tube. However, in contrast to this single-sided circuit, the corresponding push-pull form shown in Figs. 7 and 8 has the important advantages, especially the advantage that the positive feedback coupling through the coil L_2 is supplemented by positive feed coupling from the plate of each tube to its grid through the other tube (acting as an amplifier in the regenerative feedback path). Thus the phase angle of μ_1 is not entirely dependent upon the coupling between the two halves of the winding of L_2 as it would be in the single-sided arrangement. Furthermore, in the single-sided arrangement there would appear in its equivalent circuit a series term between the terminals 2 and the ideal converter C which would depend in value upon the leakage inductance in L_2 . This term would be reactive and thence introduce the possibility of singing, as explained above.

Considering Fig. 7, for example, as having terminals 1 connected in series in a line and terminals 2 connected to a network Z_N as shown in Fig. 10 described hereinafter, the converter may be viewed as a vacuum tube circuit with both negative feedback and positive feedback and with the terminals 1 serving as the input terminals and also the output terminals so the input impedance is also the output impedance. In each triode, negative feedback is produced by the impedance of the resistance R_2 at its cathode and the winding turns of transformer T_1 connected between that resistance and the negative pole of battery B. This negative feedback greatly lowers the impedance between the cathode and ground (somewhat as the feedback action in a cathode follower reduces its cathode-to-ground impedance). The lowering of the impedance between the cathode of each triode V_1 and ground results in lowering the converter input (and output) impedance appearing at terminals 1 as viewed from the line. This impedance is further lowered, and is made negative, by the positive feedback, which occurs in each triode due to the connection from its plate through the RC circuit to the grid of the other triode and the connection from the plate of that other triode through the like RC circuit to the grid of the first triode. In the circuit of each triode, and also in the converter or vacuum tube circuit as a whole, the total feedback preferably is negative, the negative feedback predominating over the positive feedback. The predominance of the negative feedback tends to stabilize the system against variation in vacuum tube constants and plate supply voltages.

Still considering Fig. 7, for example, as having terminals 1 connected in series in the line and terminals 2 connected to network Z_N as shown in Fig. 10 described hereinafter, it will be appreciated that the amplifier triodes are arranged to generate in their plate circuits a voltage, derived from the network voltage drop, which aids or boosts the line current. This aiding voltage is thus proportional to line current and will cause an increase in current over the unrepeatable condition. Because the voltage is also proportional to the network impedance, the transmission gain or current increase will be proportional to it and can be changed up or down by adjusting the network impedance up or down correspondingly.

The voltages which produce the repeater gain are obtained by feedback connection within the amplifier circuit. Voltages appearing across the network are fed back to the grids through paths comprising the coupling condensers C_1 which connect the plate of each triode to the grid of the other triode. This results in a polarity or phase for the amplified network voltage which aids the current, and so this feedback is positive feedback. Voltages appearing in the cathode circuit of each tube between cathode and ground are applied to the grid in such polarity or phase that the amplified voltage appearing in the

plate circuit opposes the line current. This feedback is negative feedback. The gain depends on the resultant of these two feedback voltages.

Elements in the grid circuit of each triode are used for controlling the feedback at the high and low frequencies so as to reduce the gain outside the range of frequencies for which gain and negative impedance are desired (for example, in the case of a telephone repeater, the range of frequencies for normal telephone usage) and increase the stability of the repeater. Coupling condensers C_1 and resistances R_1 and R_3 make combinations that reduce the positive feedback from the network at the low frequencies. Condensers C_2 and resistances R_3 and R_1 make combinations that reduce the same feedback at the high end of the desired frequency band. As noted above, the network C_1 , R_3 and R_1 largely determines the value of μ_1 at low frequencies, and the network R_3 , R_1 and C_2 largely determines the value of μ_1 at high frequencies.

The network Z_N supplements this frequency selective action, providing frequency selectivity in addition to that provided in the amplifier circuit. This serves to limit the gain to the transmission band of the particular circuit with which the network is designed or adjusted to be used, and thus serves to increase stability of the repeater (against singing). As explained hereinafter, the network further provides not only for adjusting the gain to any desired value within the allowable gain limits of the repeater, but also for equalizing or shaping the gain characteristic to compensate for the loss-frequency characteristics of the lines associated with the repeater, particularly in the case of non-loaded lines.

With terminals 1 in series in the line and terminals 2 connected to the impedance control network (gain control network) Z_N , the repeater can be monitored and tube checks made without interfering with the conversation on the line. To facilitate such tests, preferably pin jacks J_1 , J_2 and J_3 are provided, J_1 and J_2 being respectively connected to the cathodes of the two triodes V_1 in Fig. 7, and J_3 being connected to ground (i. e., to the mid-point of the circuit connecting the cathodes). These jacks are used for voltmeter connection in checking the cathode-to-ground direct-current voltages of the two triode sections of the tube. These voltage tests indicate whether the tube is operating satisfactorily and whether proper voltages are being supplied. Jacks J_1 and J_2 are used also for connecting from either J_1 or J_2 to ground (J_3) a high impedance monitoring telephone headset (about 75,000 ohms) especially designed for the repeater. When so connected, the headset is effectively across the winding of (input and output) transformer T_1 and thus monitors both directions of transmission over the line.

Any practical negative impedance converter such as that represented by Fig. 4 can be used most efficiently as a negative impedance repeater to provide gain in a transmission line either by connecting a network Z_N to terminals 2 and inserting terminals 1 in series with the line, as exemplified in Fig. 10, or by connecting a network Z_N to terminals 1 and shunting terminals 2 across the line, as exemplified in Fig. 11. In Figs. 10 and 11, the line is designated 3 and the converter 4. The converter of Fig. 7 or Fig. 7A has been designed specifically for the connection of a network Z_N to terminals 2 and the insertion of terminals 1 in series with the line, in the manner shown in Fig. 10. This converter will then introduce a reversed voltage type (i. e., series type) of negative impedance in series with the line. Practically one-half of the primary winding of transformer T_1 should be inserted in one side of the line and the other half of the winding should be inserted in the other side of the line, for proper balance against longitudinal currents (as shown in the case of winding 33 of Mathes Patent 1,779,382). The converter 4 may be, for example, as shown in Figs. 4, 5, 7 or 7A. The network Z_N may be, for example, as shown in Figs. 12, 13, 14 or 18, described hereinafter.

When the converter of Fig. 7 or Fig. 7A is used with

terminals 1 in series with a transmission line 3, the network Z_N connected to terminals 2 in the manner shown in Fig. 10 will control the negative impedance seen at terminals 1 between frequencies such as f_2 and f_3 indicated in Fig. 9, which is the band of primary interest (and thus will control the repeater gain). The preferred network for use with the converter when the repeater is employed on voice frequency transmission lines ordinarily will not be a resistance, but will consist of some combination of resistance and capacity, or resistance, capacity and inductance.

Three basic forms of such networks suitable for connection to terminals 2 of Fig. 7 or Fig. 7A are presented in Figs. 12, 13 and 14. The network configuration of Fig. 12 is suitable for use with the converter when the converter is inserted in series with a coil loaded cable circuit. When the proper values are assigned to the elements of Fig. 12 this network presents an impedance that, at frequencies between about .2 and 1.1 of the cut-off frequency f_c of the periodically loaded cable, simulates the characteristic impedance of the inductively loaded cable circuit terminated at any point in the loading section. This network is disclosed and claimed in my application, Serial No. 113,073, filed August 30, 1949, for "Electrical Network" (United States Patent 2,632,051, issued March 17, 1953). From .2 f_c to .9 f_c the network impedance closely simulates that of the line, and above .9 f_c the ratio of the resistive component of the network impedances to that of the line impedance is maintained sufficiently low to avoid instability. The basic section of the network comprises resistance R_{10} shunted by a series combination of inductance L_{10} and capacitance C_{10} , and simulates approximately the characteristic impedance, as viewed at .2 loading coil, of the periodically loaded transmission line. The network is built out to full coil by adding inductance L_{20} in series with the basic section. A building-out capacitance C_{20} across the network terminal builds out the network to any fractional sectional termination desired. The component elements of the network are evaluated in terms of the inductance of the loading coil and the capacitance and characteristic impedance of the line, and may be adjustable for use with different line facilities or end sections.

The networks of Figs. 13 and 14, when the elements of these networks are assigned appropriate values, present impedances that, when converted into their negatives and multiplied by an appropriate numeric, are suitable for insertion in a non-loaded line. (As will become apparent further on, they are not designed to simulate characteristic impedances of the associated lines.) Fig. 13 is useful in a negative impedance repeater for a non-loaded line when the line section on one side of the repeater differs in type or length from the line section on the other side. Elements R_{22} , R_{21} and C_{21} are proportioned for one of the line sections, and elements R_{11} , R_{12} and C_{12} for the other line section. Fig. 14 is useful in a negative impedance repeater when a plurality of the repeaters are used in tandem for negative impedance loading as described hereinafter. The network of this figure comprises two parts in series. One of these parts is composed of resistance R_{13} and capacitance C_{13} in series, shunted by inductance L_{13} . The other of the two parts is composed of resistance R_{23} and inductance L_{23} , shunted by capacitance C_{23} .

In many cases, especially within the area covering a city or town, known as an exchange area, a line that has already been loaded with periodically spaced series inductance coils to improve its transmission response, nevertheless can advantageously have its attenuation reduced further by the addition of a negative impedance in series in the coil loaded line. Such an addition ordinarily produces an impedance irregularity; but in many instances this irregularity is not a serious transmission impairment in coil loaded lines and its disadvantage is more than outweighed by the gain in transmission obtained by the inser-

13

tion of this negative impedance. In such cases, the negative impedance inserted in the line preferably is similar in characteristic to the negative of the characteristic impedance of the coil loaded line multiplied by a numeric which depends in value upon the return loss of the line at the point of insertion. The negative impedance may be provided, for instance, by a negative impedance repeater such as that of Fig. 10, and may, for example, comprise the negative impedance converter of Fig. 7 or Fig. 7A with a network Z_N of impedance value Z_N such as the network of Fig. 12.

A method of thus applying the negative impedance repeater to inductively loaded lines can be explained by the example shown in Fig. 15, where it is assumed for simplicity that the two periodically coil loaded line sections 5 and 6 between which the negative impedance repeater 7 is connected are identically alike, the attenuation of each being taken by way of example, as 4.5 decibels. Their far end terminations may be, for example, at central offices 8 and 9, respectively, which comprise central office switching equipment for connecting lines 5 and 6 to other circuits, as for instance, subscriber loops including subscriber stations 10 and 11. The repeater 7 may be, for example, at a third central office in the exchange area, this office being designated 12 in the drawing. The negative impedance converter of the repeater is designated 13. As just indicated, it may be the converter shown in Fig. 7 or 7A, for instance.

If line section 5 be either open-circuited at 8 or short-circuited at 8, then as the frequency in the pass band of the line is varied the impedance Z_5 seen at the repeater point (if plotted on the resistance-reactance plane) will oscillate or follow a circle which encloses the characteristic impedance Z_0 . For coil loaded exchange circuits this impedance Z_5 will go around the circle approximately once for every loading point in the line section as Z_5 is investigated over the pass band of frequencies. If the line section contains no structural or other impedance irregularities, then when the line is open-circuited or short-circuited at 8 the return loss $[20 \log_{10}(1+Z_5/Z_0)/(1-Z_5/Z_0)]$ expressed in decibels equals twice the line section attenuation, or 9 decibels in the example of Fig. 15. In Fig. 16 a circle 16 is shown plotted on the normalized impedance plane, i. e., a resistance-reactance plane whereon the abscissae are resistive or real components of the ratio Z_5/Z_0 , and the ordinates are reactive components of the ratio. In this plane the point $1 \pm j0$ equals the characteristic impedance Z_0 of any line. The circle 16 is the locus of all possible values of Z_5/Z_0 that will give a 9-decibel return loss. As shown in Fig. 16, for this 9-decibel return loss circle

$$\frac{Z_5}{Z_0}$$

will be a minimum at 0.477, or in other words, Z_5 will be a minimum at $0.477Z_0$. The impedance Z_5 will be a maximum at $2.09Z_0$. Thus for any given return loss (RL) the impedance Z_5 will have one minimum and one maximum value.

Let $-h$ designate the factor by which the impedance Z_N of network Z_N must be multiplied in order to obtain the value of the negative impedance presented to the line by the repeater (at terminals 1 of Fig. 7 or 7A, for example).

For stability, the negative impedance of the repeater ($-hZ_N$) cannot exceed $-0.477Z_0 \times 2$ if the line is to be either short-circuited or open-circuited at both ends 8 and 9, the two line sections having been assumed to be identical. The negative impedance ($-hZ_N$) has manufacturing variations. These amount to about 10 per cent so that the allowable negative impedance must be reduced by 10 per cent. Hence $-hZ_N$ cannot exceed $0.429Z_0 \times 2$ or $0.858Z_0$.

If, in the talking connection, the return loss at 8 of the subscriber loop including station 10, and the return loss

14

at 9 of the subscriber loop including station 11, each be assumed to be 6 decibels, for example, then both Z_5 and Z_6 follow the 15-decibel return loss circle 17 shown in Fig. 16 when the line is connected for subscriber use.

The variation in the insertion gain characteristic over the band of frequencies transmitted can be computed as follows. From the 15-decibel return loss circle it can be seen that the minimum value of impedance which Z_5 can have during the talking condition of the circuit is $0.696Z_0$ and the maximum value is $1.43Z_0$. If the negative impedance ($-hZ_N$) of $-0.858Z_0$ is inserted in this circuit the maximum and the minimum value of insertion gain can be found by substitution in the following equation:

$$\text{Gain in decibels} = 20 \log_{10} \left| \frac{1}{1 - \frac{hZ_N}{Z_5 + Z_6}} \right|$$

The maximum value is 8.3 decibels and the minimum value 3.1 decibels. The effective insertion gain, therefore, lies between 8.3 and 3.1 decibels. Thus the line loss of 9 decibels has been reduced by about 5 decibels.

In general, when the repeater is inserted in a coil loaded line having negligible structural or other irregularities the line loss can be reduced to about one-half its non-repeated value by the repeater gain. For terminal repeaters this reduction is slightly less than one-half. For intermediate repeaters it may be slightly more, as has just been shown.

It is noted that the negative impedance repeater introduces appreciable variation in transmission frequency response. Repeaters of the 22-type, in common use, likewise introduce such variation (though the fact is perhaps not generally appreciated).

Fig. 17 shows a negative impedance repeater 27 connecting in series a coil loaded line 25 and a non-loaded line 26. The repeater 27 may be at a central office 22. The lines 25 and 26 may connect central offices 28 and 29, for example, which may comprise switching equipment (not shown) for connecting lines 25 and 26 to other circuits, as for instance, subscriber loops (not shown). The central offices 22, 28 and 29 may be all in the same exchange area. In many cases, especially within an exchange area, a circuit comprising a coil loaded line (such as 25) and a non-loaded line (such as 26) in tandem can advantageously have the attenuation of the circuit reduced by connection of a negative impedance (such as 27) in series between the lines, as in Fig. 17, for example. In Fig. 17 the negative impedance repeater 27 may be, for instance, of the type shown in Fig. 10, and may, for example, comprise a negative impedance converter 13 of the type shown in Fig. 7 or Fig. 7A, with a network Z_N such as the network shown in Fig. 18.

The network Z_N in Fig. 18 comprises two networks 31 and 32 in series. The network 31 is shown as the network of Fig. 12 and is determined by the return loss of the loaded line 25. The network 32 is determined by the line constants per unit length and the length of the non-loaded line 26. This network 32 may be, for example, the network shown in Fig. 13 as composed of elements R_{11} , R_{12} and C_{12} .

It is to be understood that the above-described arrangements are illustrative of the application of the principles of the invention. Numerous other arrangements may be devised by those skilled in the art without departing from the spirit and scope of the invention.

What is claimed is:

1. In combination, a negative impedance converter which comprises an amplifying device having an anode, a cathode, and a control grid, a first direct current circuit path exclusive of said anode between said control grid and said cathode, a second direct-current circuit path exclusive of said control grid between said anode and said cathode, a first inductance coil common to both of said

circuit paths and forming a negative feedback impedance, a second inductance coil in said second circuit path in serial relation with said first coil, and a positive feedback path including means to supply to said control grid a voltage proportional to but reversed in phase from the voltage appearing across said second coil, the voltage amplification factor of said device and the product of said voltage amplification factor and the feedback factor of said positive feedback path both being many times greater than unity, a first terminating circuit coupled in parallel with said first coil, and a second terminating circuit coupled in parallel with said second coil, the impedance presented to either one of said terminating circuits by said converter being related to the impedance presented to said converter by the other of said terminating circuits substantially by the factor $-k$, where k is substantially a numeric over a predetermined operating frequency range.

2. In combination, a negative impedance converter which comprises an amplifying device having an anode, a cathode, and a control grid, a first direct-current circuit path exclusive of said anode between said control grid and said cathode, a second direct-current circuit path exclusive of said control grid between said anode and said cathode, a first inductance coil common to both of said circuit paths and forming a negative feedback impedance, a second inductance coil in said second circuit path in serial relation with said first coil, and a positive feedback path including means to supply to said control grid a voltage proportional to but reversed in phase from the voltage appearing across said second coil, a first terminating circuit, means coupling said first terminating circuit in parallel with said first coil, a second terminating circuit, and means coupling said second terminating circuit in parallel with said second coil, the impedance presented to either one of said terminating circuits by said converter being related to the impedance presented to said converter by the other of said terminating circuits substantially by the factor

$$\frac{-(\mu_1 - 1)}{\mu_2 + 1}$$

where μ_2 is the amplification factor of said device and μ_1 is the product of μ_2 times the ratio of the voltage fed back to said control grid by said positive feedback path to the voltage appearing across said second coil, both μ_2 and μ_1 being many times greater than unity over a predetermined operating frequency range.

3. In combination, a negative impedance converter which comprises an amplifying device having a current-emissive electrode, a current-receiving electrode, and a control electrode for current passing between said current-emissive and current-receiving electrodes, a first direct-current circuit path exclusive of said current-receiving electrode between said control electrode and said current-emissive electrode, a second direct-current circuit path exclusive of said control electrode between said current-receiving electrode and said current-emissive electrode, a first impedance common to both of said circuit paths and forming a negative feedback element, a second impedance in said second circuit path in serial relation with said first impedance, and a positive feedback path including means to supply to said control electrode a voltage proportional to but reversed in phase from the voltage appearing across said second impedance, the amplification factor of said device and the product of said amplification factor and the feedback factor of said positive feedback path both being at least several times greater than unity, a first terminating circuit parallel-coupled to said first impedance, and a second terminating circuit parallel-coupled to said second impedance, the impedance presented to either one of said terminating circuits by said converter being related to the impedance presented to said converter by the other of said terminating circuits substantially by the factor $-k$, where k is

substantially a numeric over a predetermined operating frequency range.

4. In combination, a negative impedance converter which comprises first and second amplifying devices connected in push-pull relation, each of said devices having an anode, a cathode, and a control grid, a first inductance coil connected between the cathodes of said devices and forming a negative feedback impedance, a second inductance coil connected between the anodes of said devices, a source of direct current for said devices connected between the mid-point of said first coil and the mid-point of said second coil, respective resistive connections from the control grids of said devices to said mid-point of said first coil, and respective positive feedback paths opaque to direct current connected between the anode of said first device and the control grid of said second device and between the anode of said second device and the control grid of said first device, the voltage amplification factors of each of said devices and the product of each of said voltage amplification factors and each of the feedback factors of said positive feedback paths each being many times greater than unity, a first terminating circuit coupled across said first coil, and a second terminating circuit coupled across said second coil, the impedance presented to either one of said terminating circuits by said converter being related to the impedance presented to said converter by the other of said terminating circuits substantially by the factor $-k$, where k is substantially a numeric over a predetermined operating frequency range.

5. In combination, a negative impedance converter which comprises first and second amplifying devices connected in push-pull relation, each of said devices having an anode, a cathode, and a control grid, a first inductance coil connected between the cathodes of said devices and forming a negative feedback impedance, a second inductance coil connected between the anodes of said devices, a source of direct current for said devices connected between the mid-point of said first coil and the mid-point of said second coil, respective resistive connections from the control grids of said devices to said mid-point of said first coil, and respective positive feedback paths opaque to direct current connected between the anode of said first device and the control grid of said second device and between the anode of said second device and the control grid of said first device, a first terminating circuit coupled across said first coil, and a second terminating circuit coupled across said second coil, the impedance presented to either one of said terminating circuits by said converter being related to the impedance presented to said converter by the other of said terminating circuits substantially by the factor

$$\frac{-(\mu_1 - 1)}{\mu_2 + 1}$$

where μ_2 is the amplification factor of each of said devices and μ_1 is the product of μ_2 times the ratio of the voltage fed back to the respective control grids of said devices by said positive feedback paths to the voltage appearing across each half of said second inductance coil, both μ_2 and μ_1 being many times greater than unity over a predetermined operating frequency range.

6. In combination, a negative impedance converter which comprises first and second amplifying devices connected in push-pull relation, each of said devices having a current-emissive electrode, a current-receiving electrode, and a control electrode for current passing between said current-emissive and current-receiving electrodes, a first impedance connected between the current-emissive electrodes of said devices and forming a negative feedback element, a second impedance connected between the current-receiving electrodes of said devices, a source of direct current for said devices connected between the mid-point of said first impedance and the mid-point of said second impedance, respective resistive connections from

17

the control electrodes of said devices to said mid-point of said first impedance, and respective positive feedback paths opaque to direct current connected between the current-receiving electrode of said first device and the control electrode of said second device and between the current-receiving electrode of said second device and the control electrode of said first device, a first terminating circuit coupled across said first impedance, and a second terminating circuit coupled across said second impedance, the impedance presented to either one of said terminating circuits by said converter being related to the impedance presented to said converter by the other of said terminating circuits substantially by the factor $-k$, where k is substantially a numeric over a predetermined operating frequency range.

7. In combination, a negative impedance converter which comprises first and second amplifying devices connected in push-pull relation, each of said devices having an anode, a cathode, and a control grid, a first inductance coil connected between the cathodes of said devices and forming a negative feedback impedance, a second inductance coil connected between the anodes of said devices, a source of direct current for said devices connected between the mid-point of said first coil and the mid-point of said second coil, respective resistances bypassed by capacitors connected from the control grids of said devices to said mid-point of said first coil, and respective positive feedback paths each comprising a capacitor in series with a resistance connected between the anode of said first device and the control grid of said second device and between the anode of said second device and the control grid of said first device, a first terminating circuit coupled across said first coil, and a second terminating circuit coupled across said second coil, the impedance presented to either one of said terminating circuits by said converter being related to the impedance presented to said converter by the other of said terminating circuits substantially by the factor $-k$, where k is substantially a numeric over a predetermined operating frequency range.

8. A four-terminal negative impedance converter which comprises first and second amplifying devices connected in push-pull relation, each of said devices having an anode, a cathode, and a control grid, a first inductance coil connected between the cathodes of said devices and forming a negative feedback impedance, a second inductance coil connected between the anodes of said devices, a source of direct current for said devices connected between the mid-point of said first coil and the mid-point of said second coil, respective resistive connections from the control grids of said devices to said mid-point of said first coil, respective positive feedback paths opaque to direct current connected between the anode of said first device and the control grid of said second device and between the anode of said second device and the control grid of said first device, the voltage amplification factors of each of said devices and the product of each of said voltage amplification factors and each of the feedback factors of said positive feedback paths each being many times greater than unity, a first pair of terminals coupled to the respective cathodes of said devices, and a second pair of terminals coupled to the respective anodes of said devices, the impedance presented by either pair of said terminals being related to any impedance connected across the other pair of terminals substantially by the factor $-k$, where k is substantially a numeric over a predetermined operating frequency range.

9. A four-terminal negative impedance converter which

18

comprises first and second amplifying devices connected in push-pull relation, each of said devices having an anode, a cathode, and a control grid, a first inductance coil connected between the cathodes of said devices and forming a negative feedback impedance, a second inductance coil connected between the anodes of said devices, a source of direct current for said devices connected between the mid-point of said first coil and the mid-point of said second coil, respective resistances bypassed by capacitors connected from the control grids of said devices to said mid-point of said first coil, respective positive feedback paths each comprising a capacitor in series with a resistance connected between the anode of said first device and the control grid of said second device and between the anode of said second device and the control grid of said first device, a first pair of terminals coupled to the respective cathodes of said devices, and a second pair of terminals coupled to the respective anodes of said devices, the impedance presented by either pair of said terminals being related to any impedance connected across the other pair of terminals substantially by the factor $-k$, where k is substantially a numeric over a predetermined operating frequency range.

10. A four-terminal negative impedance converter which comprises first and second amplifying devices connected in push-pull relation, each of said devices having a current-emissive electrode, a current-receiving electrode, and a control electrode for current passing between said current-emissive electrode and said current-receiving electrode, a first impedance connected between the current-emissive electrodes of said devices and forming a negative feedback element, a second impedance connected between the current-receiving electrodes of said devices, a source of direct current for said devices connected between the midpoint of said first impedance and the mid-point of said second impedance, respective resistive connections from the control electrodes of said devices to said mid-point of said first impedance, respective positive feedback paths opaque to direct current connected between the current-receiving electrode of said first device and the control electrode of said second device and between the current-receiving electrode of said second device and the control electrode of said first device, a first pair of terminals coupled to the respective current-emissive electrodes of said devices, and a second pair of terminals coupled to the respective current-receiving electrodes of said devices, the impedance presented by either pair of said terminals being related to any impedance connected across the other pair of terminals substantially by the factor $-k$, where k is substantially a numeric over a predetermined operating frequency range.

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